FEB 0 2 2007 W

Docket No.: Applicant: P04761

John E. Gavlik et al. 40.: 09/713,389

Application No.: Filing Date:

November 15, 2000

REPLACEMENT

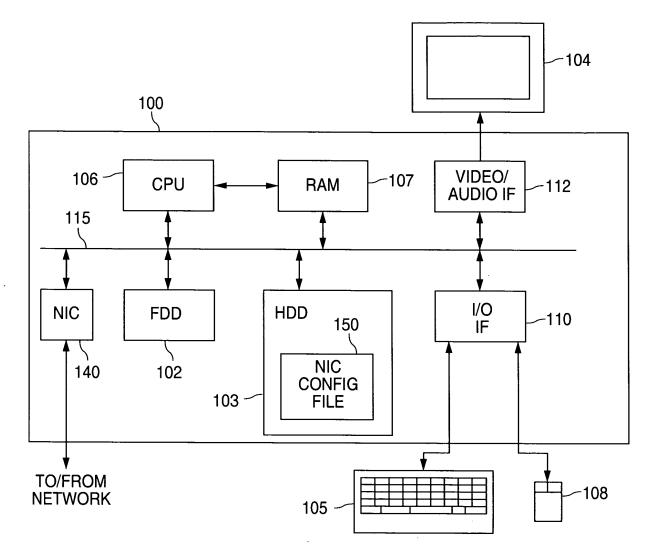
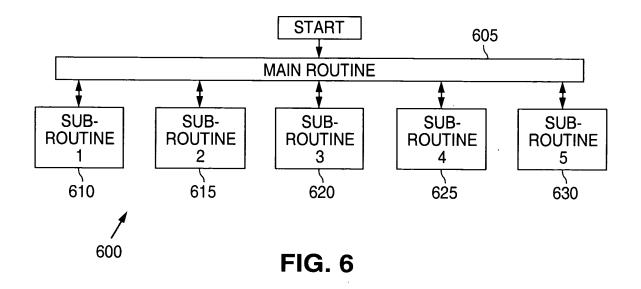


FIG. 1



REPLACEMENT

Docket No.:

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RJ45 240 MAG 230 140 10-100-1000 ETHERNET PHY LAYER CONTROLLER RAM (16K) 220 ROM (16K) 260~ MII/GMII MDC MD 60 10-100-1000 ETHERNET MAC LAYER PCI BUS 250-

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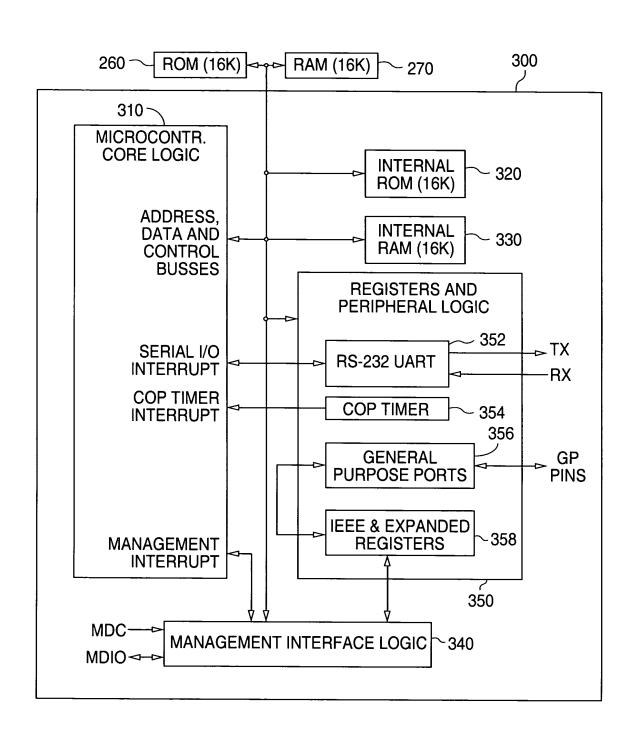


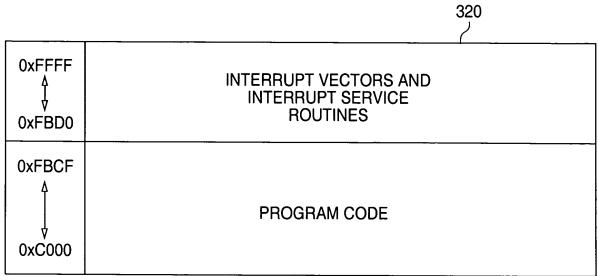
FIG. 3

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REPLACEMENT



0xBFFF A V 0xBF80	STACK
0xBF7F	PATCH CODE & ADDITIONAL VARIABLES
0x83FF	POINTERS, VARIABLES & USER-DEFINED CODE
	330A

		
0xBFFF A V 0xBF80	STACK	
0xBF7F	PATCH CODE & ADDITIONAL VARIABLES	
0xA0FF	DOWNLOADED INTERRUPT SERVICE ROUTINES	
0x9DCF	DOWNLOADED PROGRAM CODE	
0x83FF A V 0x8000	POINTERS, VARIABLES & USER-DEFINED CODE	
330B		

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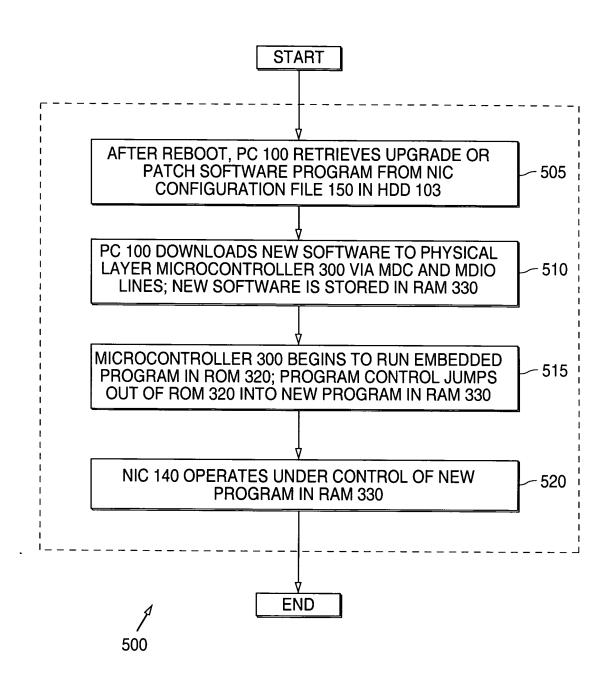


FIG. 5

REPLACEMENT

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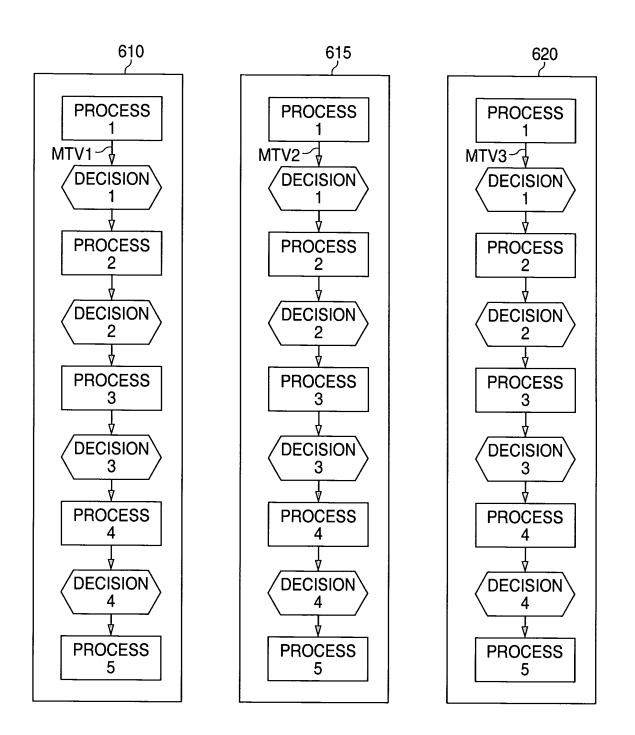


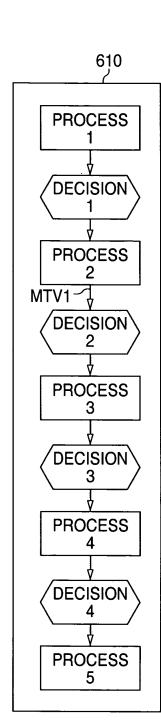
FIG. 7

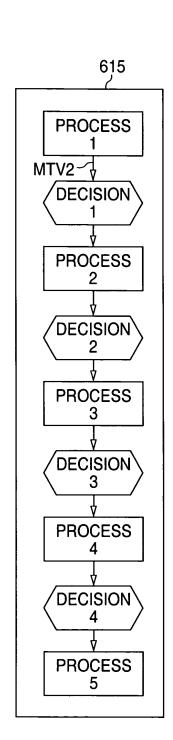
Docket No.: Applicant:

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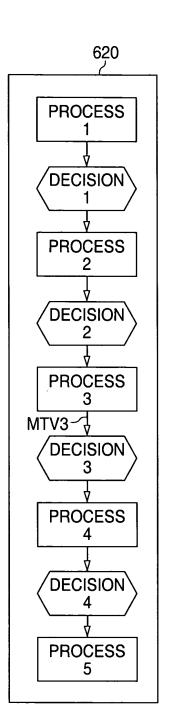


FIG. 8

REPLACEMENT

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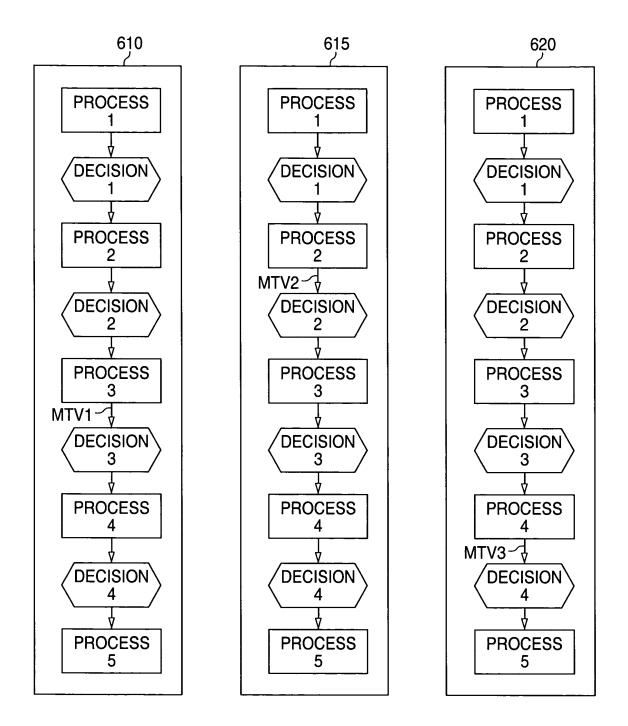


FIG. 9